



Docket No. 07977/052001

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT application of)
Hisashi OHTANI et al.)
Serial No. 08/690,747) Art Unit:1109
Filed: August 1, 1996) Examiner: E. Defillo
For: METHOD FOR MANUFACTURING)
SEMICONDUCTOR DEVICE)

VERIFICATION OF TRANSLATION

Honorable commissioner of patents and Trademarks
Washington, D.C. 20231

Sir:

I, Noriko Honda, B-301, 394-1, Hase, Atsugi-shi,
Kanagawa-ken 243-0036 Japan, a translator, herewith declare:

that I am well acquainted with both the Japanese and
English Languages;

that I am the translator of the attached translation of the
Japanese Patent Application No. 7-216608 filed on August 2, 1995;
and

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that to the best of my knowledge and belief the following is a true and correct translation of the Japanese Patent Application No. 7-216608 filed on August 2, 1995.

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date: this 9th day of February, 1999

Noriko Honda

Name: Noriko Honda

[Name of Document]	Patent Application	
[Reference Number]	P003053-01	
[Filing Date]	August 2, 1995	
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[International Patent Classification]	H01L 21/00	
[Title of Invention]	Method for Manufacturing a Semiconductor Device	
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[Attachment]	Specification	1
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